OIPE IAP58
OCT 24 2007
PATENT & TRADEMARK OFFICE

FIG. 1

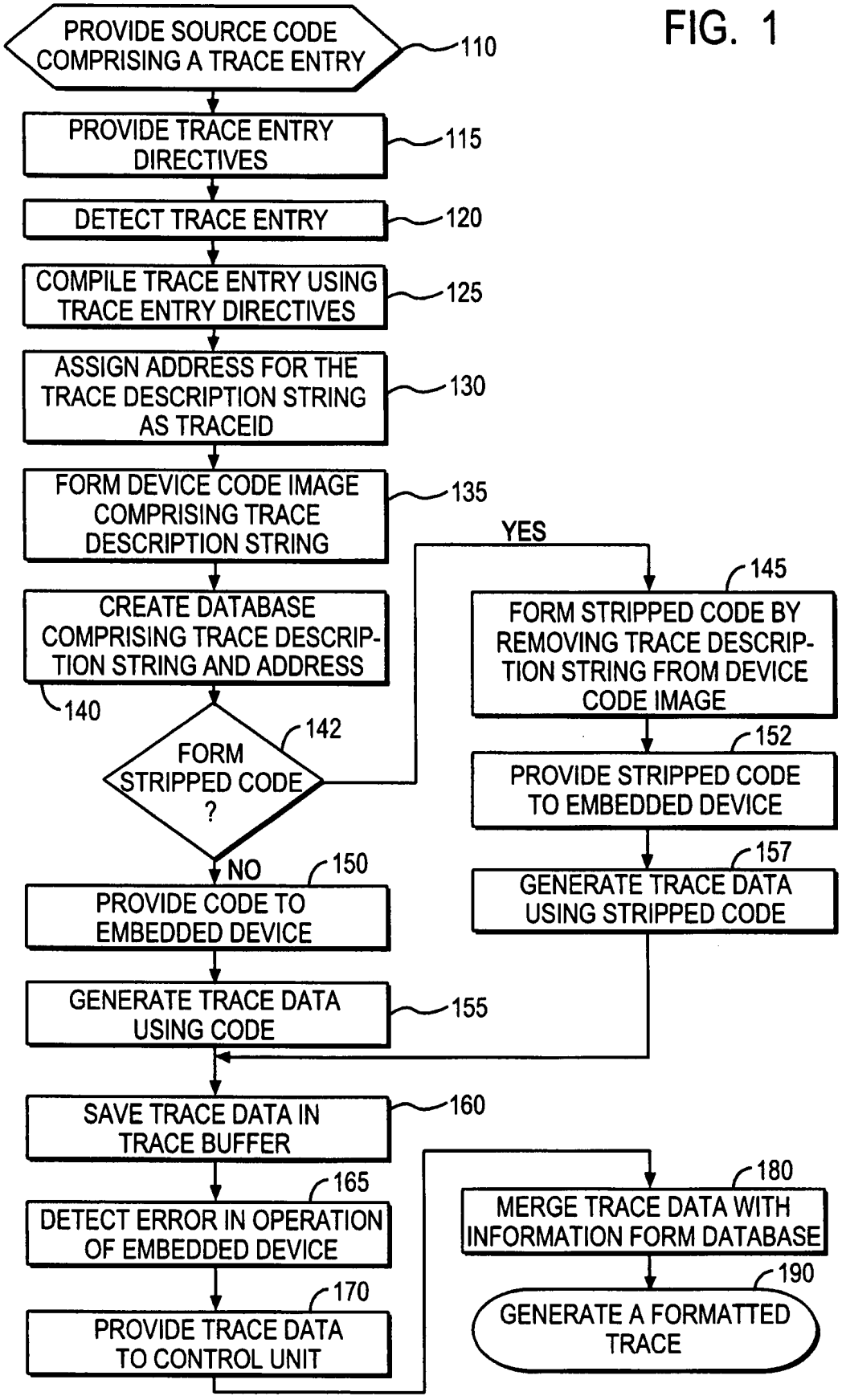


FIG. 2

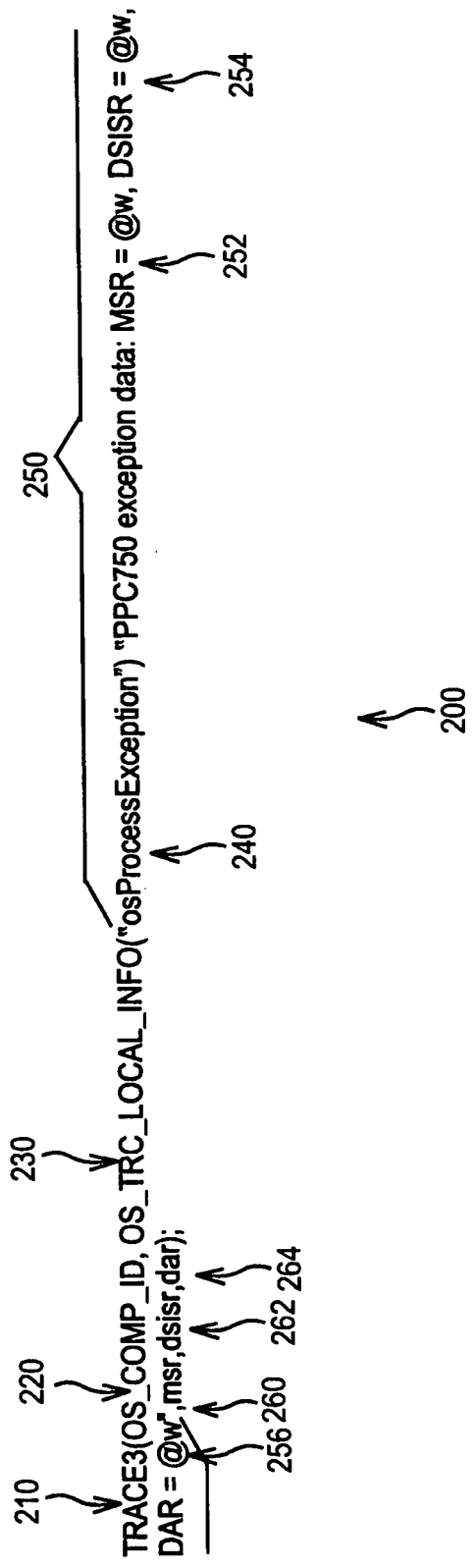

TRACE3(OS_COMP_ID, OS_TRC_LOCAL_INFO("osProcessException") "PPC750 exception data: MSR = @w, DSISR = @w,
DAR = @w",msr,dsisr,dar);
210
220
230
240
250
252
254
256
260
262
264
200

FIG. 3

```
#define TRACE_MAGIC_PREFIX "LPTRACE_PRE"

#define TRACE_MAGIC_SUFFIX "LPTRACE_SUF"

#define TRACEID(str) (TRACE_MAGIC_PRE str "TRACE_MAGIC_SUF)

#define TRACE3(compIdIn, traceStrin, data1In, data2In, data3In) \
        osiTrace3(compIdIn, TRACEID("3"traceStrIn),(UINT32)(data1In), (UINT32)(data2In), (UINT32)(data3In))

#define OS_LNTSTR1(lineIn) #lineIn

#define OS_LNTSTR(lineIn) OS_LNTSTR1(lineIn)

#define OS_TRC_LOCAL_INFO(functionNameIn) \
        _FILE_"," OS_LNTSTR(__LINE__)"," functionNameIn ":"
```

300

FIG. 4

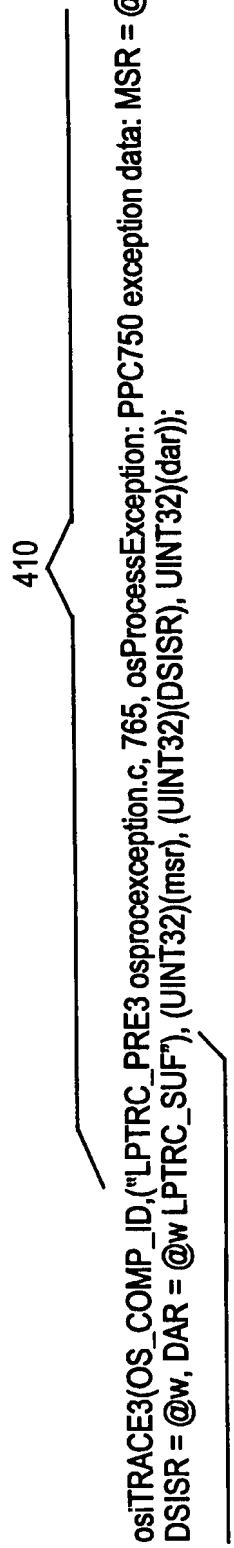

410
osiTRACE3(OS_COMP_ID,("LPTRC_PRE3 osprocexception.c, 765, osProcessException: PPC750 exception data: MSR = @w,
DSISR = @w, DAR = @w LPTRC_SUF"), (UINT32)(msr), (UINT32)(DSISR), UINT32)(dar));

FIG. 5

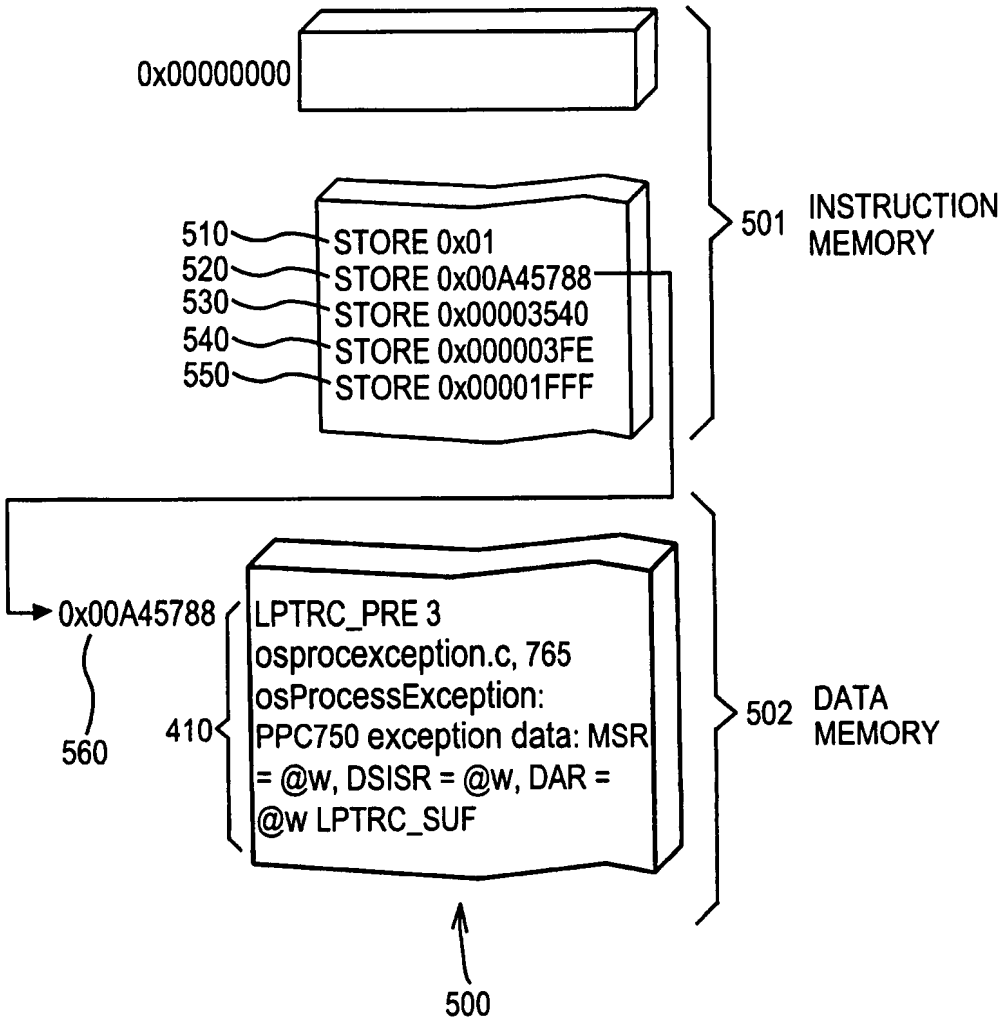

0x00000000
510
520
530
540
550
STORE 0x01
STORE 0x00A45788
STORE 0x00003540
STORE 0x000003FE
STORE 0x00001FFF
501
INSTRUCTION MEMORY
0x00A45788
560
410
LPTRC_PRE 3
osprocexception.c, 765
osProcessException:
PPC750 exception data: MSR = @w, DSISR = @w, DAR = @w LPTRC_SUF
502
DATA MEMORY
500

FIG. 6

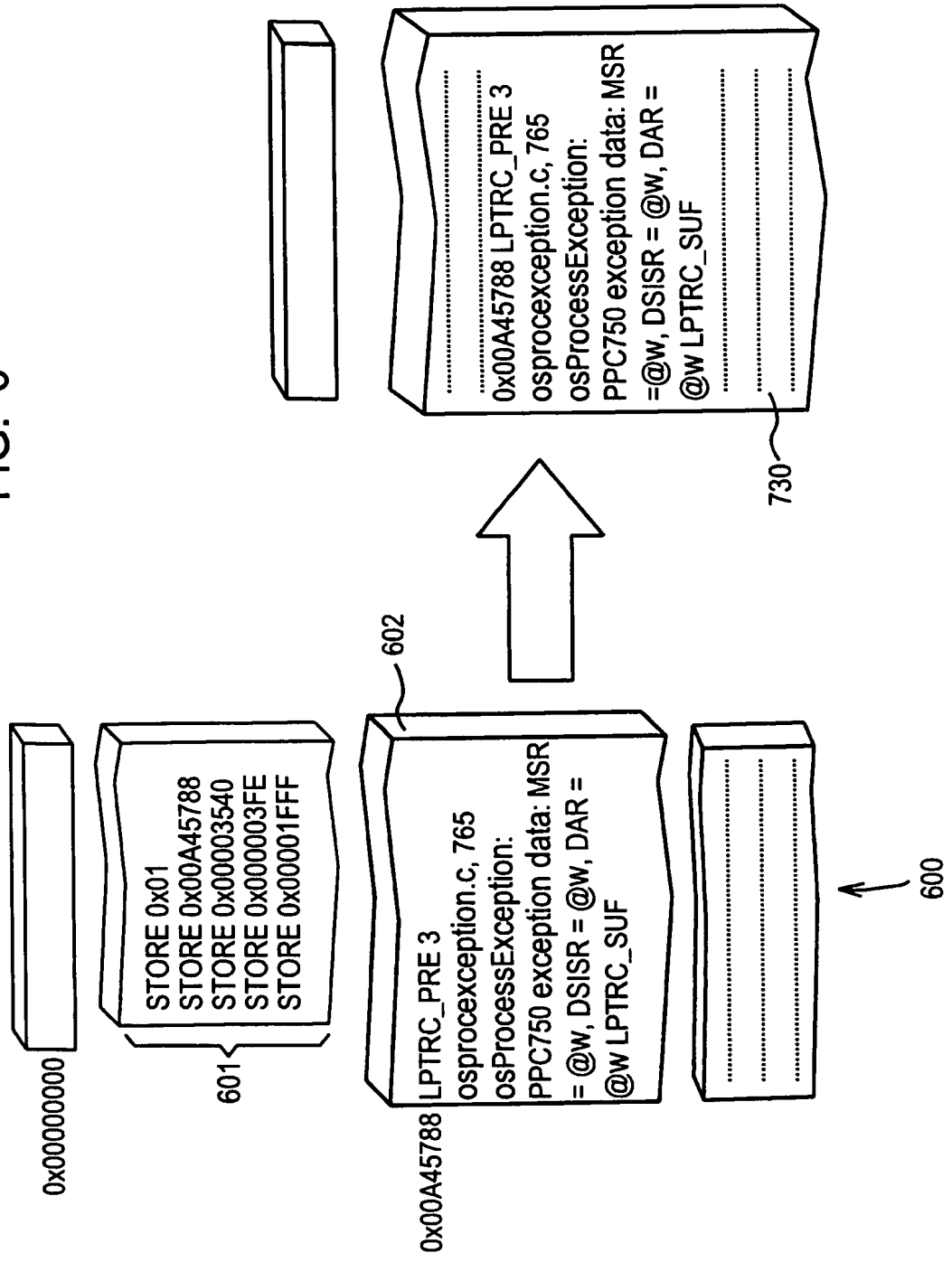

0x00000000
STORE 0x01
STORE 0x00A45788
STORE 0x00003540
STORE 0x000003FE
STORE 0x00001FFF
601
0x00A45788
LPTRC_PRE 3
osprocexception.c, 765
osProcessException:
PPC750 exception data: MSR
= @w, DSISR = @w, DAR =
@w LPTRC_SUF
602
0x00A45788 LPTRC_PRE 3
osprocexception.c, 765
osProcessException:
PPC750 exception data: MSR
=@w, DSISR = @w, DAR =
@w LPTRC_SUF
730
600

FIG. 7

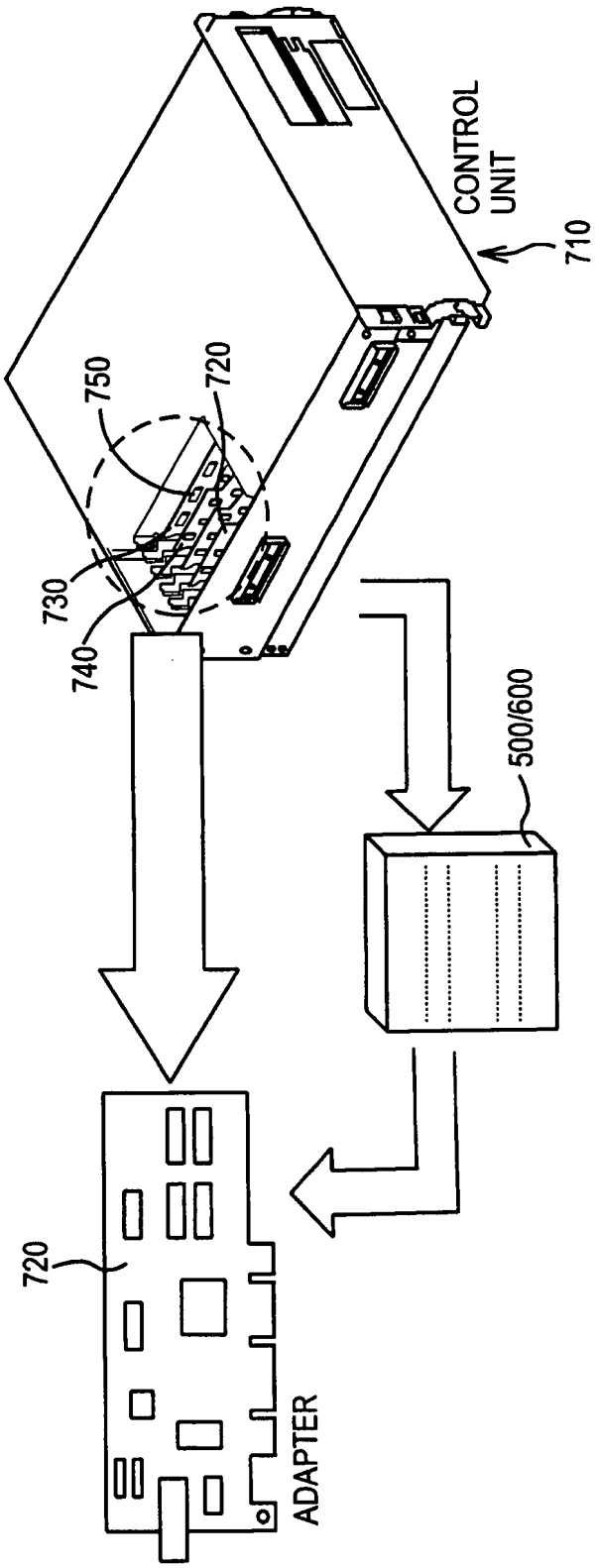
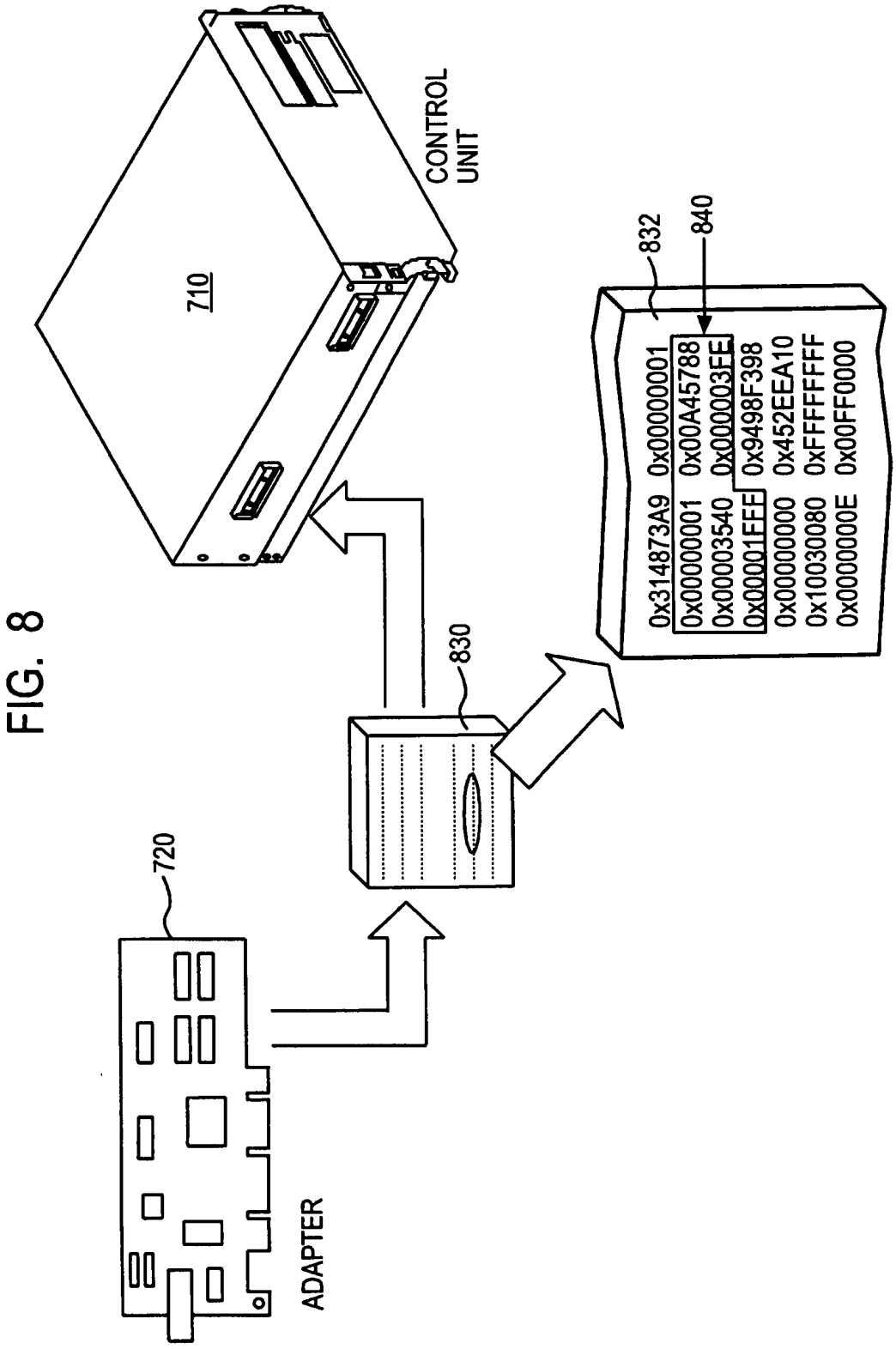


FIG. 8


710
CONTROL UNIT
832
840
0x314873A9 0x00000001
0x00000001 0x00A45788
0x00003540 0x000003EE
0x00001FEF 0x9498F398
0x00000000 0x452EEA10
0x10030080 0xFFFFFFFF
0x0000000E 0x00FF0000
830
720
ADAPTER

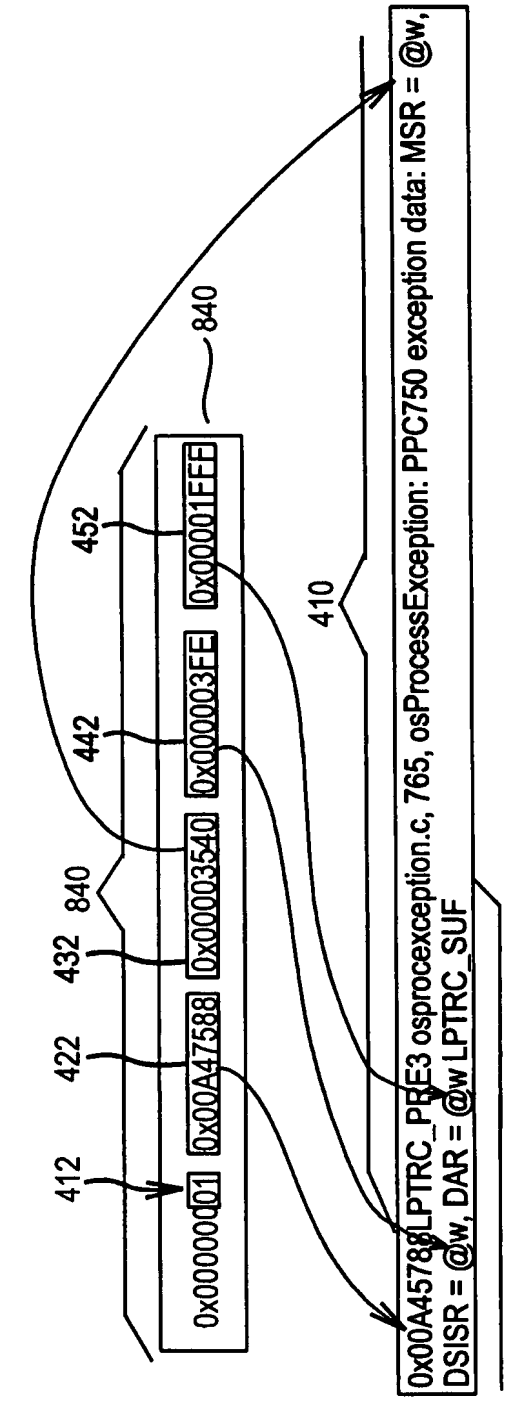

412
422
432
840
442
452
840
410
0x00000001
0x00A47588
0x00003540
0x000003EE
0x00001EEE
0x00A45788 LPTRC_PRE3 osprocexception.c, 765, osProcessException: PPC750 exception data: MSR = @w,
DSISR = @w, DAR = @w LPTRC_SUF

FIG. 9

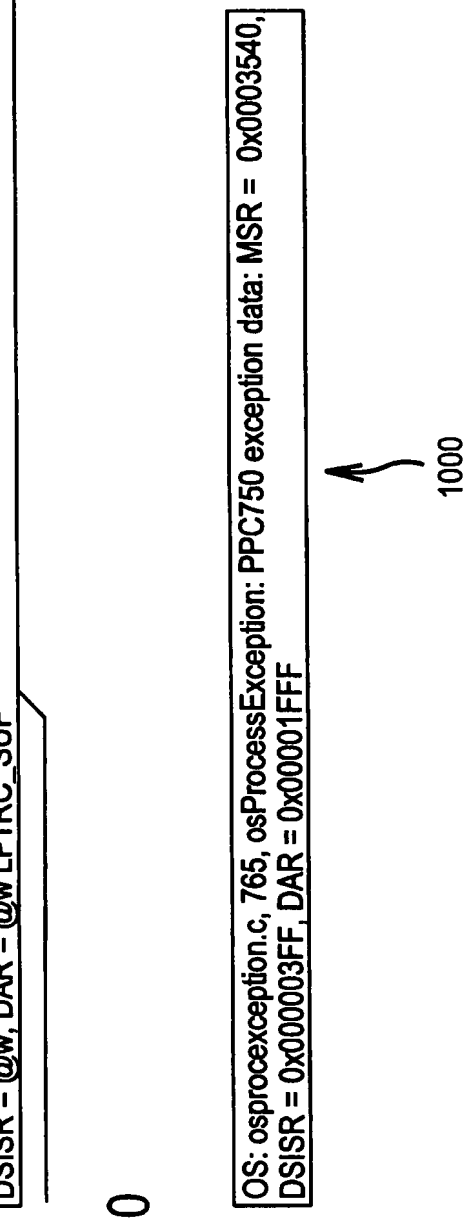

OS: osprocexception.c, 765, osProcessException: PPC750 exception data: MSR = 0x0003540,
DSISR = 0x000003FF, DAR = 0x00001FFF
1000

FIG. 10